

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L1	83	"712".clas. and stall same (status or valid) same register same data	US-PGPUB; USPAT; EPO	OR	OFF	2005/06/22 13:13
L2	43	"712".clas. and stall with (status or valid) same register same data	US-PGPUB; USPAT; EPO	OR	OFF	2005/06/22 13:13
L3	17	miss adj recovery	US-PGPUB; USPAT; EPO	OR	OFF	2005/06/22 13:13
L4	1	register adj status same stall	US-PGPUB; USPAT; EPO	OR	OFF	2005/06/22 13:14
L5	103	conditional adj store	US-PGPUB; USPAT; EPO	OR	OFF	2005/06/22 13:14
L6	77	register with valid with stall	US-PGPUB; USPAT; EPO	OR	OFF	2005/06/22 13:15
L7	228360	exception\$1	US-PGPUB; USPAT; EPO	OR	OFF	2005/06/22 13:15
L8	491	712/217.ccls.	US-PGPUB; USPAT; EPO	OR	OFF	2005/06/22 13:15
L9	89	8 and (scoreboard\$3 or (dependence adj tracking) or (dataflow adj tracking) or (exception adj defer\$4))	US-PGPUB; USPAT; EPO	OR	OFF	2005/06/22 13:18
L10	264	712/219.ccls.	US-PGPUB; USPAT; EPO	OR	OFF	2005/06/22 13:18
L11	17	"712".clas. and @ad<"20001204" and ((reference adj tracking) or (exception adj deferral) or (dataflow adj tracking) or (dependency adj tracking))	US-PGPUB; USPAT; EPO	OR	OFF	2005/06/22 13:28
L12	48	712/27.ccls.	US-PGPUB; USPAT; EPO	OR	OFF	2005/06/22 13:26
L13	109	712/25.ccls.	US-PGPUB; USPAT; EPO	OR	OFF	2005/06/22 13:26
L14	140	712/201.ccls.	US-PGPUB; USPAT; EPO	OR	OFF	2005/06/22 13:26

L15	99	14 and register	US-PGPUB; USPAT; EPO	OR	OFF	2005/06/22 13:27
L16	22	712/201.ccls. and register near5 status	US-PGPUB; USPAT; EPO	OR	OFF	2005/06/22 13:28
L17	101	"712".clas. and @ad<"20001204" and ((register with (valid or status)) same stall)	US-PGPUB; USPAT; EPO	OR	OFF	2005/06/22 13:36
L18	84	17 and cache	US-PGPUB; USPAT; EPO	OR	OFF	2005/06/22 13:34
L19	67	"712".clas. and @ad<"20001204" and ((pipeline with stall) with (valid or invalid))	US-PGPUB; USPAT; EPO	OR	OFF	2005/06/22 13:37
L20	2004	"712".clas. and @ad<"20001204" and ((valid or invalid or status) same load\$3)	US-PGPUB; USPAT; EPO	OR	OFF	2005/06/22 13:40
L21	1204	"712".clas. and @ad<"20001204" and ((valid or invalid or status) with load\$3)	US-PGPUB; USPAT; EPO	OR	OFF	2005/06/22 13:41
L22	385	"712".clas. and @ad<"20001204" and ((valid or invalid or status) with load\$3) and register.ab.	US-PGPUB; USPAT; EPO	OR	OFF	2005/06/22 13:44
L23	13	"712".clas. and @ad<"20001204" and ((valid or invalid or status)with load\$3 same stall) and register.ab.	US-PGPUB; USPAT; EPO	OR	OFF	2005/06/22 13:47
L24	48	"712".clas. and @ad<"20001204" and ((valid or invalid or status)with stall) and register.ab.	US-PGPUB; USPAT; EPO	OR	OFF	2005/06/22 13:47
L25	301	"712".clas. and @ad<"20001204" and (pipeline near register)	US-PGPUB; USPAT; EPO	OR	OFF	2005/06/22 13:48
L26	329	"712".clas. and @ad<"20001204" and (pipeline near (register or buffer))	US-PGPUB; USPAT; EPO	OR	OFF	2005/06/22 13:49
L27	15	712/217.ccls. and @ad<"20001204" and (register adj status)	US-PGPUB; USPAT; EPO	OR	OFF	2005/06/22 13:51
L28	4	712/217.ccls. and @ad<"20001204" and (blocking adj cache)	US-PGPUB; USPAT; EPO	OR	OFF	2005/06/22 13:52
L29	34	"712".clas. and @ad<"20001204" and (blocking adj cache)	US-PGPUB; USPAT; EPO	OR	OFF	2005/06/22 13:52
L30	4	"712".clas. and @ad<"20001204" and ((invalid adj data) same recover\$1)	US-PGPUB; USPAT; EPO	OR	OFF	2005/06/22 13:54

L31	10	"712".clas. and @ad<"20001204" and ((pipeline adj register) same stall)	US-PGPUB; USPAT; EPO	OR	OFF	2005/06/22 13:54
L32	91	"712".clas. and @ad<"20001204" and ((cache adj miss) same (memory and stall))	US-PGPUB; USPAT; EPO	OR	OFF	2005/06/22 13:55
L33	44	"712".clas. and @ad<"20001204" and ((cache adj miss) same (recovery))	US-PGPUB; USPAT; EPO	OR	OFF	2005/06/22 14:16
L34	19	"712".clas. and @ad<"20001204" and ((conditional adj store))	US-PGPUB; USPAT; EPO	OR	OFF	2005/06/22 14:16
L35	103	conditional adj store	US-PGPUB; USPAT; EPO	OR	OFF	2005/06/22 14:16
L36	9	"712".clas. and @ad<"20001204" and (write adj ordering)	US-PGPUB; USPAT; EPO	OR	OFF	2005/06/22 14:17
L37	20	multiple adj status adj bits	US-PGPUB; USPAT; EPO	OR	OFF	2005/06/22 14:17